

Fig. 1

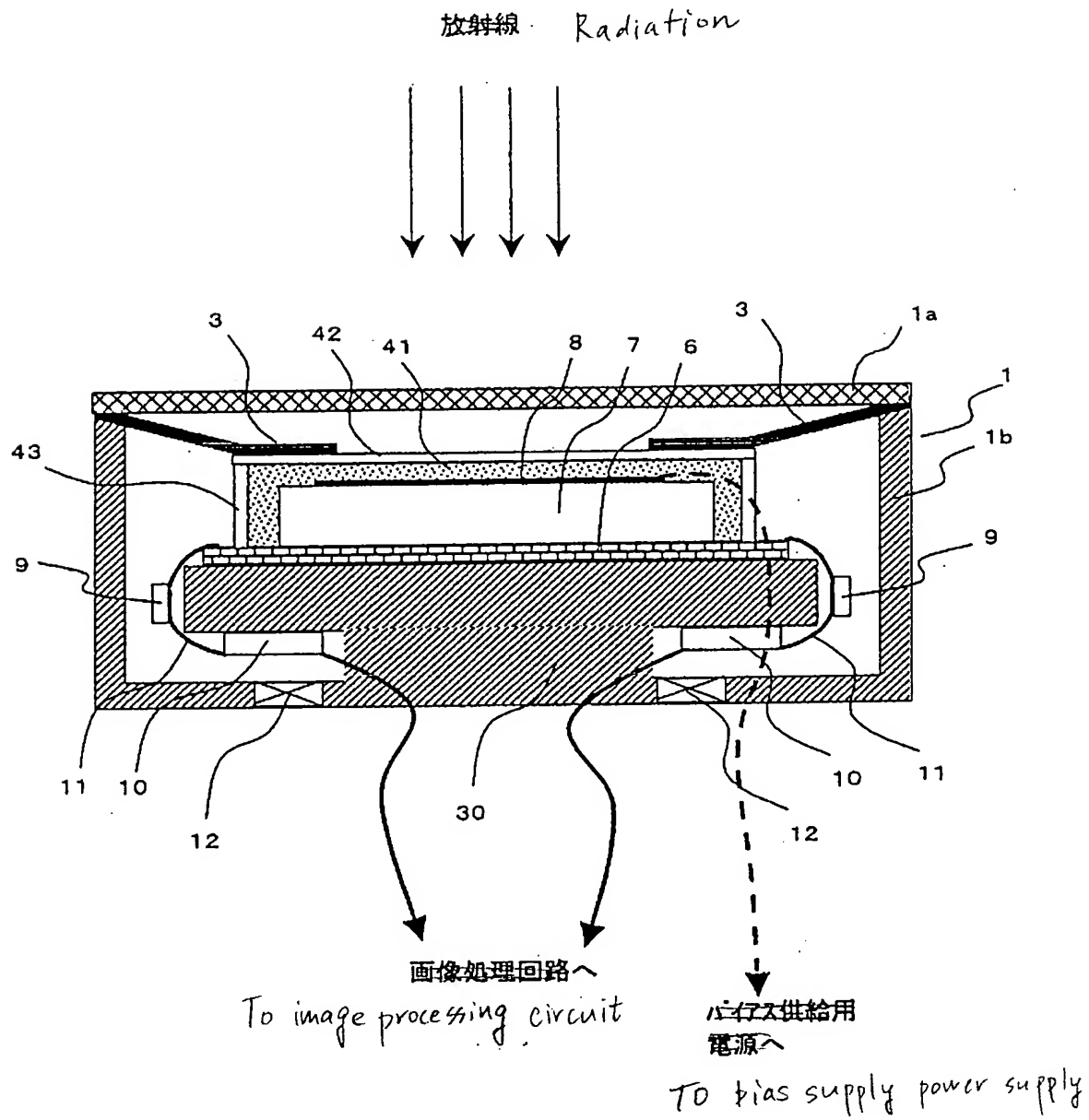
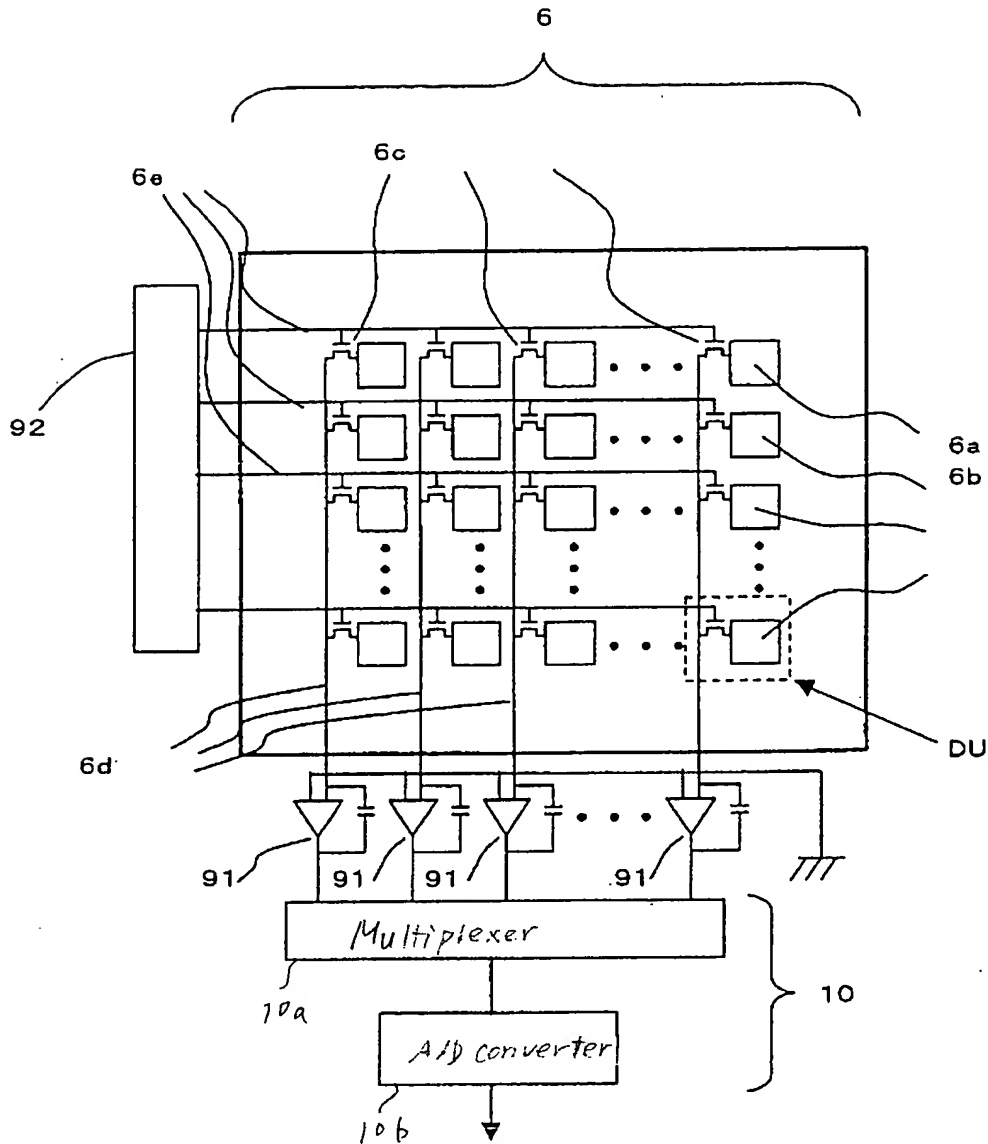


Fig. 2



画像処理回路へ

To image processing circuit

Fig. 3

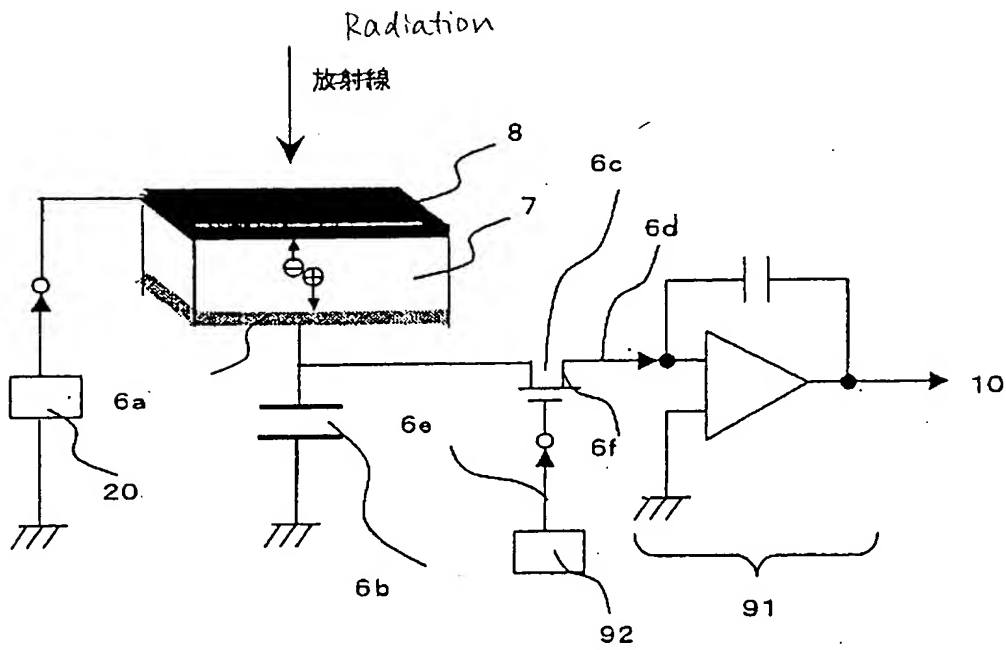


Fig. 4

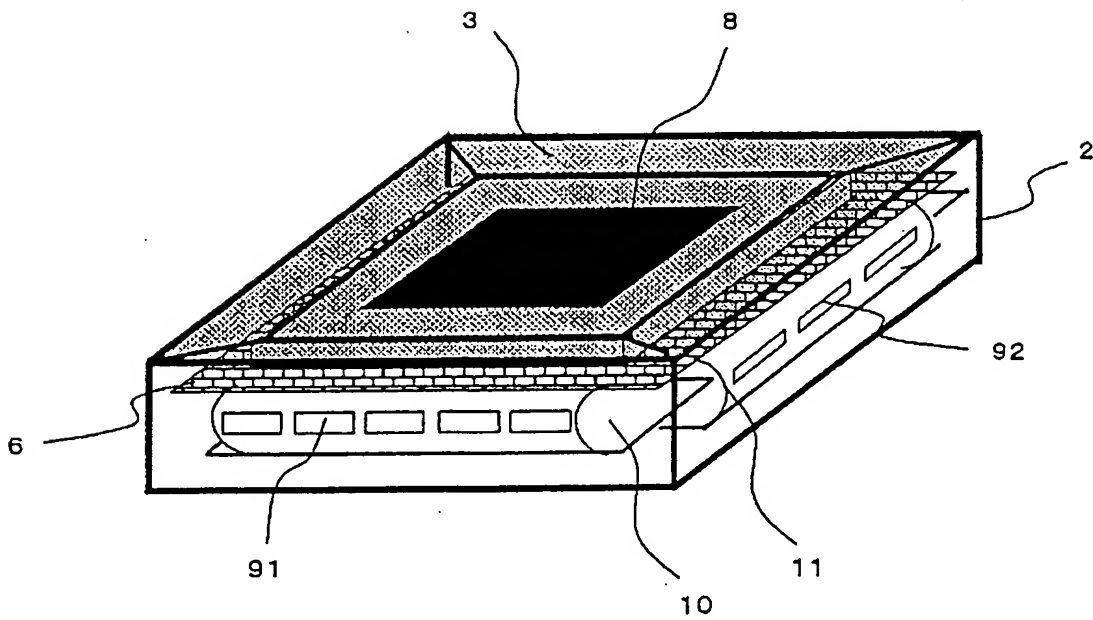


Fig. 5

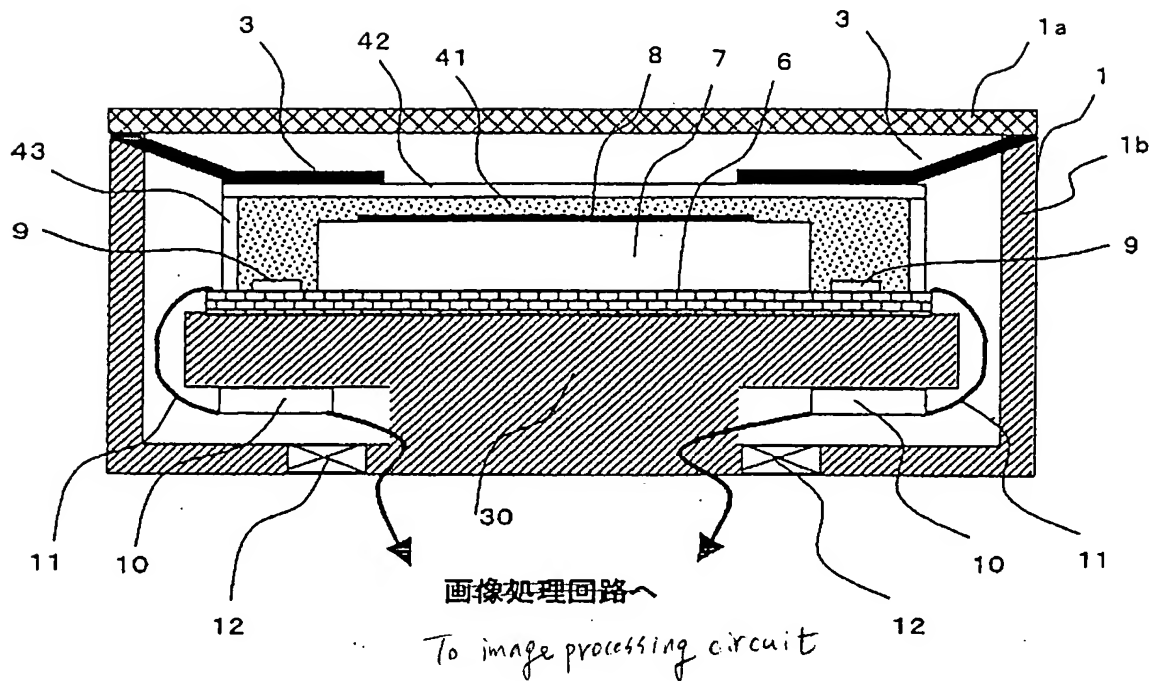


Fig. 6

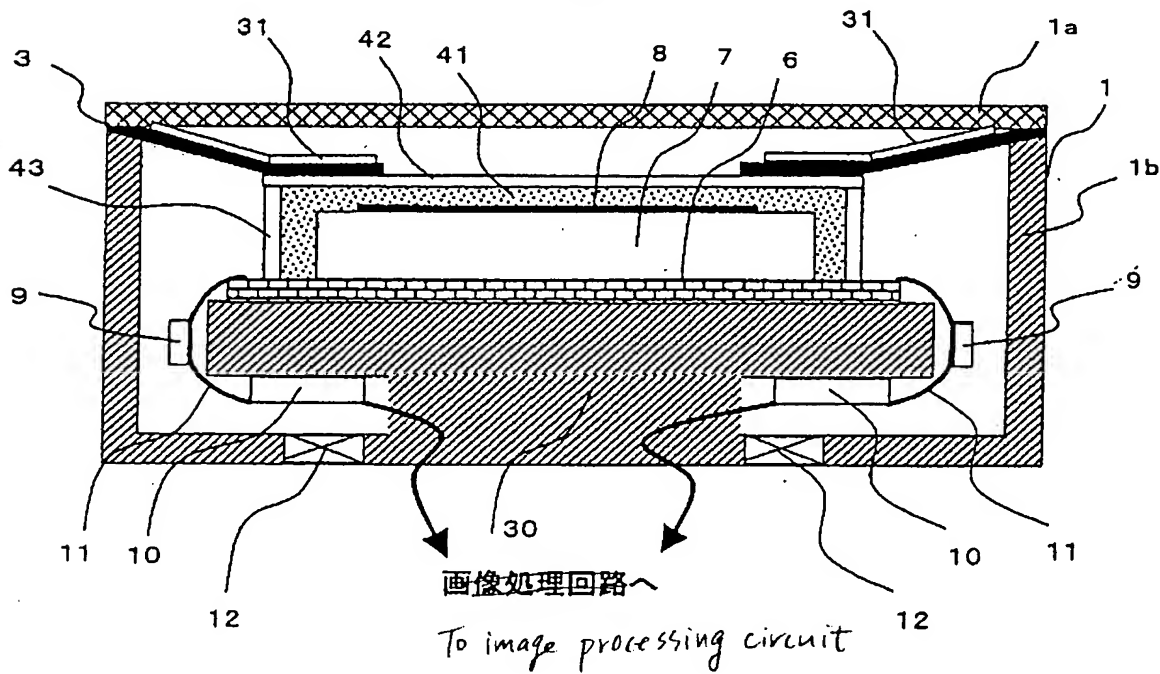


Fig. 7

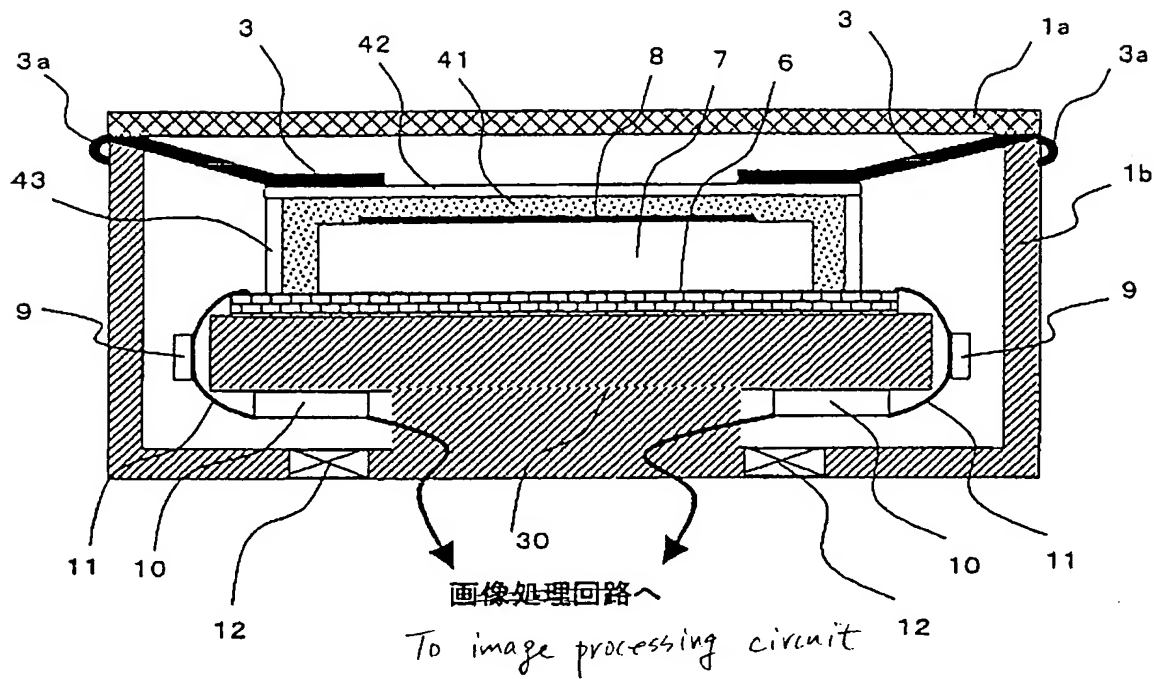


Fig. 8

